

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

FUJIMOTO ET AL

National phase of International Application No. PCT/JP2004/001157

International filing date: February 4, 2004

Date of entry into national phase: August 2, 2005

For: METHOD OF INTERCONNECTING TERMINALS AND  
METHOD OF MOUNTING SEMICONDUCTOR DEVICES

PRELIMINARY AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Prior to examination, please amend the referenced  
application as shown below:

IN THE TITLE:

Please rewrite the title indicated on the front page of the international publication of the present application so as to read as follows:

~~METHOD FOR INTERCONNECTING TERMINALS AND METHOD FOR MOUNTING SEMICONDUCTOR DEVICE~~ METHOD OF INTERCONNECTING TERMINALS AND METHOD OF MOUNTING SEMICONDUCTOR DEVICES